

PCT

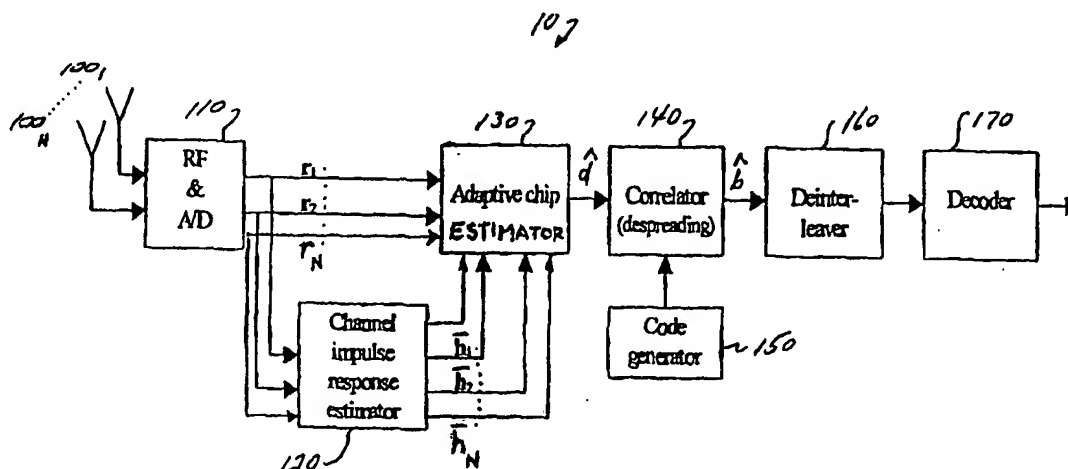
WORLD INTELLECTUAL PROPERTY ORGANIZATION  
International Bureau



INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

|                                                                                                                                                    |  |    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
|----------------------------------------------------------------------------------------------------------------------------------------------------|--|----|------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| (51) International Patent Classification <sup>7</sup> :<br>H04B 3/04, 1/06                                                                         |  | A1 | (11) International Publication Number: WO 00/54427                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                       |
|                                                                                                                                                    |  |    | (43) International Publication Date: 14 September 2000 (14.09.00)                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                        |
| (21) International Application Number: PCT/FI00/00193                                                                                              |  |    | (81) Designated States: AE, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CR, CU, CZ, DE, DK, DM, DZ, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, TZ, UA, UG, UZ, VN, YU, ZA, ZW, ARIPO patent (GH, GM, KE, LS, MW, SD, SL, SZ, TZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG). |
| (22) International Filing Date: 10 March 2000 (10.03.00)                                                                                           |  |    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| (30) Priority Data:<br>60/123,689 10 March 1999 (10.03.99) US<br>09/517,882 3 March 2000 (03.03.00) US<br>09/518,922 6 March 2000 (06.03.00) US    |  |    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| (71) Applicant: NOKIA MOBILE PHONES LTD. [FI/FI];<br>Keilalahdentie 4, FIN-02150 Espoo (FI).                                                       |  |    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| (72) Inventors: KOMULAINEN, Petri; Hallituskatu 25 B 30,<br>FIN-90100 Oulu (FI). HEIKKILÄ, Markku; Kalevalantie<br>17 A AS 8, FIN-90570 Oulu (FI). |  |    |                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                                          |
| (74) Agent: BERGGREN OY AB; P.O. Box 16, FIN-00101<br>Helsinki (FI).                                                                               |  |    | Published<br>With international search report.<br>Before the expiration of the time limit for amending the<br>claims and to be republished in the event of the receipt of<br>amendments.                                                                                                                                                                                                                                                                                                                                                                                                                                                 |

(54) Title: ADAPTIVE CHANNEL EQUALIZER



(57) Abstract

A receiver for use in a CDMA telecommunications system is disclosed. The receiver includes at least one antenna for receiving signals from a CDMA channel, an estimator for estimating an impulse response of the channel, and an adaptive equalizer. The estimator provides a reference for the adaptive equalizer, and the adaptive equalizer operates to estimate a transmitted chip sequence of the channel and to restore orthogonality among the received signals. The adaptive equalizer includes circuitry for utilizing a blind adaptive algorithm to estimate the transmitted chip sequence of the channel.

**FOR THE PURPOSES OF INFORMATION ONLY**

Codes used to identify States party to the PCT on the front pages of pamphlets publishing international applications under the PCT.

|    |                          |    |                                          |    |                                              |    |                          |
|----|--------------------------|----|------------------------------------------|----|----------------------------------------------|----|--------------------------|
| AL | Albania                  | ES | Spain                                    | LS | Lesotho                                      | SI | Slovenia                 |
| AM | Armenia                  | FI | Finland                                  | LT | Lithuania                                    | SK | Slovakia                 |
| AT | Austria                  | FR | France                                   | LU | Luxembourg                                   | SN | Senegal                  |
| AU | Australia                | GA | Gabon                                    | LV | Larvia                                       | SZ | Swaziland                |
| AZ | Azerbaijan               | GB | United Kingdom                           | MC | Monaco                                       | TD | Chad                     |
| BA | Bosnia and Herzegovina   | GE | Georgia                                  | MD | Republic of Moldova                          | TG | Togo                     |
| BB | Barbados                 | GH | Ghana                                    | MG | Madagascar                                   | TJ | Tajikistan               |
| BE | Belgium                  | GN | Guinea                                   | MK | The former Yugoslav<br>Republic of Macedonia | TM | Turkmenistan             |
| BF | Burkina Faso             | GR | Greece                                   |    |                                              | TR | Turkey                   |
| BG | Bulgaria                 | HU | Hungary                                  | ML | Mali                                         | TT | Trinidad and Tobago      |
| BJ | Benin                    | IE | Ireland                                  | MN | Mongolia                                     | UA | Ukraine                  |
| BR | Brazil                   | IL | Israel                                   | MR | Mauritania                                   | UG | Uganda                   |
| BY | Belarus                  | IS | Iceland                                  | MW | Malawi                                       | US | United States of America |
| CA | Canada                   | IT | Italy                                    | MX | Mexico                                       | UZ | Uzbekistan               |
| CF | Central African Republic | JP | Japan                                    | NE | Niger                                        | VN | Viet Nam                 |
| CG | Congo                    | KE | Kenya                                    | NL | Netherlands                                  | YU | Yugoslavia               |
| CH | Switzerland              | KG | Kyrgyzstan                               | NO | Norway                                       | ZW | Zimbabwe                 |
| CI | Côte d'Ivoire            | KP | Democratic People's<br>Republic of Korea | NZ | New Zealand                                  |    |                          |
| CM | Cameroon                 |    |                                          | PL | Poland                                       |    |                          |
| CN | China                    | KR | Republic of Korea                        | PT | Portugal                                     |    |                          |
| CU | Cuba                     | KZ | Kazakstan                                | RO | Romania                                      |    |                          |
| CZ | Czech Republic           | LC | Saint Lucia                              | RU | Russian Federation                           |    |                          |
| DE | Germany                  | LI | Liechtenstein                            | SD | Sudan                                        |    |                          |
| DK | Denmark                  | LK | Sri Lanka                                | SE | Sweden                                       |    |                          |
| EE | Estonia                  | LD | Lithuania                                | SG | Singapore                                    |    |                          |

**ADAPTIVE CHANNEL EQUALIZER**

This is a continuation in part of U.S. Patent Application 09/\_\_\_\_\_, entitled "Adaptive Channel Equalizer," by Petri Komulainen and Markku J. Heikkilä, Attorney Docket 872.8546 USU, filed March 3, 2000, Express Mail No.:EL 470231293US, which claims priority under 35 U.S.C. §119(e) from Provisional Patent Application 60/123,689, filed March 10, 1999, entitled "Adaptive Channel Equalizer," by Petri Komulainen and Markku J. Heikkilä. The disclosure of this Provisional Patent Application is incorporated by reference herein in its entirety.

**FIELD OF THE INVENTION:**

This invention relates generally to communications systems, and, in particular to a receiver that performs adaptive channel equalization.

**BACKGROUND OF THE INVENTION:**

CDMA systems are based on a digital, wideband, spread spectrum technology which transmits multiple, independent user signals across an allocated segment of the radio spectrum. In CDMA, each user signal includes a different orthogonal code and a pseudo random binary sequence that modulates a carrier, spreading the spectrum of the waveform, and thus allowing a large number of user signals to share the same frequency spectrum. The user signals are separated in the receiver with a correlator which allows only the signal with the selected orthogonal code to be despread. The other user signals, whose codes do not match, are not despread, and as such, contribute to system noise. The signal to noise ratio of the system is determined by the ratio of the desired signal power to the sum of all interfering signals, enhanced by the system processing gain and the ratio of the spread bandwidth to the baseband data rate.

This also applies to the 3rd generation wideband CDMA (WCDMA) standards currently proposed, where different spreading factors and variable user data rates can be supported simultaneously, that is, various users are multiplexed to the channel typically by orthogonal spreading codes. In CDMA systems in general, however, due to multipath propagation and frequency-selective fading, the orthogonality between the various users' waveforms is degraded, and multiple access interference impairs the performance of the receiver.

The interference generated by the other user signals is called multiple access interference, and is essentially caused by the amount of user traffic in the channel, which corrupts the code orthogonality at the receiver.

5 Several multiuser detection schemes have been proposed to alleviate multiple access interference in the uplink of direct-sequence code division multiple access (DS-CDMA) systems. These methods, however, are too complex and burdensome to be incorporated into a conventional user terminal because they would negatively impact its power consumption and size.

10 Reference in this regard may be had to: A. Duel-Hallen, J. Holtzman, and Z. Zvonar, "Multiuser detection for CDMA systems," *IEEE Personal Communications*, pp. 46-58, April 1995.

15 As an alternative, several adaptive algorithms with low computational complexity have been proposed to alleviate multiple access interference in CDMA systems. For example, in the presence of a known training sequence a least mean square (LMS) algorithm can be applied. However, when no training sequence is available, or training is impractical, blind adaptation schemes are better suited for CDMA interference reduction.

20 Reference in this regard may be had to: M. L. Honig, U. Madhow, and S. Verdú, "Blind adaptive multiuser detection," *IEEE Transactions on Information Theory*, vol. 41, pp. 944-960, July 1995. Reference may also be had to: N. Zecevic and J. H. Reed, "Blind adaptation algorithms for direct-sequence spread-spectrum CDMA single-user detection," *IEEE International Vehicular Technology Conference, VTC'97*, May 1997, pp. 2133-2137.

25 However, most of the proposed adaptive interference suppression methods rely on the symbol level cyclostationarity of the signal. Unfortunately, this excludes their application to systems employing long scrambling codes, such as systems employing wideband CDMA (WCDMA), wherein the pseudorandom scrambling changes the correlation properties of the signal from symbol to symbol.

#### **OBJECTS AND ADVANTAGES OF THE INVENTION:**

30 It is an object and advantage of this invention to provide a receiver which utilizes an adaptive channel equalization function to restore the orthogonality between synchronous users' waveforms and to suppress other multiple access signals.

It is a further object and advantage of this invention to apply adaptive channel equalization to a CDMA system by first estimating a transmitted chip sequence, and which then utilizing an algorithm to converge to a linear minimum mean-square error (LMMSE) solution.

- 5 It is a further object and advantage of this invention to provide a CDMA receiver with improved performance which is suited for systems using long scrambling codes, such as third generation WCDMA systems.

It is a further object and advantage of this invention to provide a CDMA receiver with improved performance which does not require training sequences or training  
10 information, with the exception of ordinary channel estimation, which may be based on either dedicated pilot symbols, or a pilot channel.

#### **SUMMARY OF THE INVENTION:**

The foregoing and other problems are overcome and the objects of the invention are realized by methods and apparatus in accordance with embodiments of this  
15 invention.

A receiver for use in a CDMA telecommunications system is disclosed. The receiver includes at least one antenna for receiving signals from a CDMA channel, an estimator for estimating an impulse response of the channel, and an adaptive equalizer. The estimator provides a reference for the adaptive equalizer, and the  
20 adaptive equalizer operates to estimate a transmitted chip sequence of the channel, to restore the orthogonality between synchronous users' waveforms, and to suppress other multiple access signals. The adaptive equalizer includes circuitry for implementing a blind adaptive algorithm to estimate the transmitted chip sequence of the channel.

25 A receiver for use in a CDMA telecommunications system is disclosed. The receiver includes at least one antenna, an estimator for estimating an impulse response of a channel, and an adaptive equalizer. The estimator provides a reference for the adaptive equalizer, and the adaptive equalizer operates to equalize a signal in the channel by estimating the transmitted chip sequence. The effect of the adaptive  
30 linear receiver algorithm is to restore the orthogonality of the signal's waveform.

The adaptive equalizer preferably utilizes a blind adaptive algorithm to estimate the transmitted chip sequence of the channel and to converge to a linear minimum

**BRIEF DESCRIPTION OF THE DRAWINGS:**

The above set forth and other features of the invention are made more apparent in the ensuing Detailed Description of the Invention when read in conjunction with the attached Drawings, wherein:

- 5 Figure 1 shows a block diagram of a receiver in accordance with an aspect of this invention.

Figure 2 shows a block diagram of an adaptive estimation filter in accordance with an aspect of this invention, where  $N=1$  (single antenna) and  $N_s=1$  (no oversampling).

- 10 Figure 3 shows the BER performance curves of the receiver using a spreading factor  $G=32$  and a single receiver antenna.

Figure 4 shows the BER performance curves using a spreading factor  $G=32$  and two receiver antennas.

- 15 Figure 5 shows the BER performance curves when using a spreading factor  $G=4$  and a single receiver antenna.

Figure 6 shows the BER performance curves when a spreading factor  $G=4$  has been used with two receiver antennas.

Figure 7 shows the effects on the BER of an interfering base station when a spreading factor  $G=4$  has been used with two receiver antennas.

- 20 **DETAILED DESCRIPTION OF THE INVENTION:**

- As stated above, in a synchronous CDMA downlink using orthogonal spreading codes, multiple access interference is essentially caused by the multipath channel. The multiple access interference then, can be suppressed by channel equalization because the users' orthogonal signals propagate through the same channel to the  
25 receiver of interest.

Reference in regard to channel equalization may be had to: D. P. Taylor, G. M. Vivetta, B. D. Hart, and A. Mämmelä, "Wireless channel equalization," *European Transactions on Telecommunications*, vol. 9, no. 2, pp. 117-143, 1998.

Further reference may be had to S. Werner and J. Lilleberg, "Downlink channel

Conference, VTC'99, May 1999. Reference may also be had to: P. Komulainen and M. J. Heikkilä, "Adaptive channel equalization based on chip separation for CDMA downlink," *IEEE International Symposium on Personal, Indoor and Mobile Radio Communications*, PIMRC'99, September 1999.

- 5 A nonzero delay spread of the frequency-selective channel causes signal leakage between the users according to the cross-correlation properties of the spreading waveforms. The interpath interference of a single user may be significant, especially when processing gains are low, and this interference may be reduced by channel equalization.
- 10 In an aspect of the invention, a receiver 10 is disclosed having an adaptive linear interference suppression algorithm. The algorithm is based on linear channel equalization and is suitable for synchronous CDMA systems using orthogonal spreading codes with long code scrambling. The algorithm is adaptive in that it converges to the linear minimum mean-square error solution by applying Griffiths'
- 15 algorithm. The receiver does not require any training sequence or training information, with the exception of ordinary channel estimation, which can be based on either dedicated pilot symbols or a pilot channel.

Figure 1 shows a block diagram of the receiver 10 in accordance with an aspect of this invention. At least one antenna  $100_1, \dots, 100_N$  receives signals from a CDMA telecommunications channel. The signals are coupled to a conventional RF transceiver and A/D converter 110. The conventional RF transceiver and A/D converter 110 may optionally perform chip waveform filtering. The converted signals  $r_1, \dots, r_N$  are coupled to a channel impulse response estimator 120 and to an

20 adaptive chip estimator 130. The channel impulse response estimator 120 operates to estimate the impulse response of the CDMA channel and provide reference input vectors  $\bar{h}_1, \dots, \bar{h}_N$  to the adaptive chip estimator 130. The adaptive chip estimator 130 may be a linear filter, which is described in detail below. The output of the adaptive chip estimator 130 is coupled to a symbol-synchronized code correlator 140. The correlator 140 despreads the output  $d$  of the adaptive chip estimator 130 by

25 multiplying it with the output of a code generator 150, and then integrating over the symbol period. The code generator 150 is capable of generating long and short codes. The output of the correlator 140 is coupled to a conventional deinterleaver 160 which in turn is coupled to a conventional decoder 170 which outputs data decisions.

30

## SIGNAL MODEL

In a CDMA downlink with  $K$  symbol-synchronous users, the base station generates the sum of the users' signals and scrambles it by using a long pseudonoise (PN) code,  $c(i)$ . The period of the complex scrambling code can extend over an entire frame of symbols. A so-called multiuser chip sequence may be defined as:

$$d(i) = c(i) \sum_{m=1}^K \sum_{k=1}^K a_k(m) b_k(m) s_k(i - mG) \quad (1)$$

where  $i$ ,  $m$ , and  $k$  are the chip, symbol, and user index, respectively. The real and positive amplitude  $a_k(m)$  is due to power control, and  $b_k(m)$  is a quadrature phase shift keying (QPSK) data symbol of user  $k$ . Spreading code  $s_k$  is assumed to be a Walsh code and, thus, the spreading codes of the users are (ideally) orthogonal. The spreading factor (processing gain) can be denoted by  $G$ ,  $s_k(i) = \pm 1$  for  $i = 0, 1, \dots, G-1$ , and zero otherwise. Clearly, the multiuser chip sequence is formed as the sum of all users' chips. Due to the long PN code scrambling,  $\{d(i)\}_i$  is a sequence of uncorrelated complex signal elements, and due to the user-dependent power control, its amplitude distribution is unknown to the receiver.

To limit the required bandwidth, sequence  $\{d(i)\}_i$  is transmitted after filtering by a pulse shaping filter with impulse response  $p(\tau)$ . Assuming that the channel impulse response during chip interval  $i$  is  $g(\tau, i)$ , the overall impulse response experienced by the chip sequence is  $h(\tau, i) = g(\tau, i) * p(\tau - iT_c)$ , where  $T_c$  is the chip interval and  $*$  denotes convolution. For a vector presentation, the response is time-discretized by taking  $N_s$  samples per chip interval to obtain impulse response vector  $\mathbf{h}(i)$ . Now, the received signal vector can be written as

$$\mathbf{r} = \mathbf{H}\mathbf{d} + \mathbf{n}, \quad (2)$$

where

$$\mathbf{H} = (\dots \mathbf{h}(-1) \mathbf{h}(0) \mathbf{h}(1) \dots) \quad (3)$$

and

$$\mathbf{d} = \begin{pmatrix} \vdots \\ d(-1) \\ d(0) \\ d(1) \\ \vdots \end{pmatrix}. \quad (4)$$



Matrix  $\mathbf{H}$  includes both the effect of the pulse shaping filter and the channel. It can also be considered to represent the effect of a receiver front-end filter (not shown in Figure 1). Vector  $\mathbf{n}$  is an arbitrary noise or interference vector assumed to be uncorrelated with  $\mathbf{d}$ . A truncated form of equation (2) may be defined as:

$$5 \quad \mathbf{r}(i) = \begin{pmatrix} r(iN_s - M_1) \\ \vdots \\ r(iN_s) \\ \vdots \\ r(iN_s + M_2) \end{pmatrix} = \mathbf{H}(i)\mathbf{d} + \mathbf{n}(i). \quad (5)$$

Sample index  $iN_s$  is defined as the index of the sample with which the pulse conveying  $d(i)$  begins. Clearly,  $\mathbf{H}(i)$  and  $\mathbf{n}(i)$  simply hold the required rows of  $\mathbf{H}$  and  $\mathbf{n}$ , respectively. Constants  $M_1$  and  $M_2$  are real and positive and define the length of the linear channel equalizer defined in the sequel. This is illustrated in Figure 2  
10 for the case where  $M_1 + M_2 = L$ .

### LMMSE CHIP ESTIMATION

In this section, the linear fractionally spaced channel equalizer, which is optimal in the minimum mean-square sense, is defined. The approach is to suppress the multiple access interference by striving to restore the orthogonality of the users' waveforms at the receiver by estimating the transmitted multiuser chips  $d(i)$ , and  
15 thus by equalizing the channel. With a perfectly estimated chip sequence, the desired user's signal can be restored without any residual interference from the other users by correlating the multiuser chip sequence with the long scrambling code and the short code of the user.

20 It is possible to describe the linear minimum mean-square error (LMMSE) channel equalizer using the signal model of equation (5).

Reference in this regard may be had to: S. M. Kay, *Fundamentals of Statistical Signal Processing: Estimation Theory*, Prentice Hall, New Jersey, 1993.

By using the presented signal model in equation (5), the linear minimum mean-square error (LMMSE) channel equalizer can be written as  
25

$$\hat{d}(i) = \bar{\mathbf{h}}^H(i) \left( \mathbf{H}(i)\mathbf{H}^H(i) + \frac{1}{\sigma_d^2} \mathbf{C}_{nn} \right)^{-1} \mathbf{r}(i) \quad (6)$$

where  $(\cdot)^H$  denotes Hermitian transpose,  $\bar{\mathbf{h}}(i)$  is the column of  $\mathbf{H}(i)$  that corresponds to  $d(i)$ ,  $\sigma_d^2$  is the average power of the uncorrelated multiuser chips, and  $\mathbf{C}_{nn}$  is the noise covariance matrix. Clearly, the LMMSE solution in equation (6) requires estimation of the channel impulse response and the covariance matrix. In addition,  
 5 the required matrix inversion may result in unacceptable computational complexity in a fast changing environment.

### ADAPTIVE ALGORITHM

Because the direct solution for the equalizer suffers from high computational complexity an adaptive method is next considered to adjust the coefficients of the  
 10 transversal filter. The problem of the adaptive approach is that no training multiuser chip sequence is available.

In the following, a blind adaptive algorithm is defined, which does not require any training sequence but only ordinary channel estimation. An implementation of the blind adaptive algorithm as included in the adaptive chip estimator 130 is shown in  
 15 Figure 2 for the case of a single antenna ( $N=1$ ) without oversampling ( $N_S=1$ ). The output  $\mathbf{r}_1$  of the RF transceiver and A/D converter 110 (Figure 1) is shown here in Figure 2 as input  $\mathbf{r}(i)$ . The output  $\bar{\mathbf{h}}_1 = (h_0, \dots, h_L)^T$  of the channel impulse response estimator 120 (Figure 1) is also an input to the adaptive chip estimator 130. The operation of the adaptive chip estimator 130 will now be described in detail.

20 Let the estimate of  $d(i)$  in the case of a single receiving antenna (omitting the antenna index) be:

$$\hat{d}(i) = \mathbf{w}^H(i) \mathbf{r}(i). \quad (7)$$

The algorithm to update the complex coefficients of the fractionally spaced multiuser chip estimator, also referred to as the adaptive chip estimator 130, is

$$\begin{aligned} \mathbf{w}(i+1) &= \mathbf{w}(i) + \mu(i) \left( \mathbf{E} \{ \mathbf{r}(i) d^*(i) \} - \mathbf{r}(i) \hat{d}^*(i) \right) \\ &= \mathbf{w}(i) + \mu(i) \left( \sigma_d^2 \bar{\mathbf{h}}(i) - \mathbf{r}(i) \hat{d}^*(i) \right) \end{aligned} \quad (8)$$

where  $\mathbf{E}(\cdot)$  denotes expectation and  $(\cdot)^*$  complex conjugate,  $\mu(i)$  is a possibly time varying step size parameter, and  $\bar{\mathbf{h}}(i)$  is the column of  $\mathbf{H}(i)$  which corresponds to  $d(i)$  in equation (5). The need for training sequence is avoided by using the knowledge of the correlation between the desired signal (multiuser chip) and the  
 30 received signal. For this, the receiver 10 employs a channel estimator to estimate the

required in practice, as this factor affects only the scaling of the filter output. A presently preferred algorithm, given in equations (7) and (8), is referred to as Griffiths' algorithm.

- Reference in this regard may be had to J. R. Treichler, C. R. Johnson, Jr., and M. G. Larimore, *Theory and Design of Adaptive Filters*, John Wiley & Sons, 1987.

The implementation of Griffiths' algorithm is a modification of an LMS algorithm. By examining the steady-state of equation (8) it can be seen that the adaptation converges to the Wiener solution.

- Returning to Figure 1, as stated above, the output of the adaptive chip estimator 130 is fed to the symbol-synchronized code correlator 140 which despreads and descrambles the signal and outputs symbol decisions. In the beginning of operation, for example, upon initialization, the filter coefficients can be initialized by the channel impulse response, and thus the receiver resembles an ordinary RAKE receiver.
- In a general case the receiver may have several antennas. Generalization of equation (7) and equation (8) to an  $N$ -antenna system yields

$$\hat{d}(i) = (\mathbf{w}_1^H(i) \dots \mathbf{w}_N^H(i)) \begin{pmatrix} \mathbf{r}_1(i) \\ \vdots \\ \mathbf{r}_N(i) \end{pmatrix} \quad (9)$$

$$\begin{pmatrix} \mathbf{w}_1(i+1) \\ \vdots \\ \mathbf{w}_N(i+1) \end{pmatrix} = \begin{pmatrix} \mathbf{w}_1(i) \\ \vdots \\ \mathbf{w}_N(i) \end{pmatrix} + \mu(i) \begin{pmatrix} \sigma_d^2 \bar{\mathbf{h}}_1(i) - \mathbf{r}_1(i) \hat{d}^*(i) \\ \vdots \\ \sigma_d^2 \bar{\mathbf{h}}_N(i) - \mathbf{r}_N(i) \hat{d}^*(i) \end{pmatrix} \quad (10)$$

- where the vectors are defined as earlier and the additional subscripts refer to the antenna index. The structure of the adaptive receiver 10 with multiple antennas can be seen in Figure 1.

## PERFORMANCE EVALUATION

- In this section, the performance of the disclosed receiver using adaptive channel equalization is examined and compared to an ordinary RAKE receiver. Consider a WCDMA downlink using QPSK modulation with  $K$  active, equal power users each

as a scrambling code. No error correction coding is used in the performance simulations.

The receivers are tested in a time-varying Rayleigh channel with delay spread of 1  $\mu$ s and with three resolvable paths of equal average power. A chip rate of 4 MHz, root raised cosine pulse shape filtering, a carrier frequency of 2 GHz, and a vehicle speed of 5 km/h are used in the simulations. Both single-antenna and two-antenna receivers are simulated. In the two-antenna case, the antennas are assumed to be uncorrelated. Perfect estimates of the channel impulse response are given to the receivers.

Figures 3 and 4 show the BER performance curves with a spreading factor  $G=32$  and with one and two receiver antennas, respectively. Note that  $E_b/N_0$  is measured per antenna element. Similarly, Figures 5 and 6 show the corresponding curves when spreading factor  $G=4$  has been used. In all the figures, several values of the number of users are tested.

In Figure 7, the effect of an interfering base station on both the receivers is presented. The interference power is assumed to be equal to the received power of the desired base station, and a three-path fading channel (with a different delay profile) is used as the interference generator.

The results show that the disclosed adaptive receiver can suppress multiple access interference efficiently. With a spreading factor of  $G=4$ , the channel equalization approach gives a performance gain in the case of a single-user because the interpath interference is significant. As the number of users increases, the channel equalization works more and more efficiently as the signal-to-noise ratio of the multiuser chips gets higher. Clearly, the adaptive equalizer can utilize the whole transmission power of the base station. With diversity antennas the performance difference between RAKE and the adaptive receiver is even larger. Furthermore, the LMMSE solution exploits the knowledge of the covariance of the interference, and thus the disclosed adaptive receiver tolerates structured intercell interference considerably better than conventional RAKE receivers.

It should be understood that the functions described herein may be implemented with discrete circuit elements, or as software routines that are executed by a suitable data processor. A combination of circuit elements and software routines may also be employed. Also, while the use of Griffiths' algorithm is disclosed as a presently

preferred embodiment, other algorithms that provide an adaptive complex chip estimation filter may be used.

Thus, while the invention has been particularly shown and described with respect to preferred embodiments thereof, it will be understood by those skilled in the art that  
5 changes in form and details may be made therein without departing from the scope and spirit of the invention.

**CLAIMS**

What is claimed is:

1. A receiver for use in a CDMA telecommunications system, comprising:
  - at least one antenna for receiving users' signals from a CDMA channel;
  - 5 an estimator, coupled to said antenna, for estimating an impulse response of said channel;
  - an adaptive equalizer coupled to said antenna and further coupled to said estimator, wherein said estimator provides a reference for said adaptive equalizer, and said adaptive equalizer operates to estimate a transmitted chip
  - 10 sequence of said channel and thus restoring an orthogonality among said received users' signals; and
  - a correlator, coupled to said adaptive separator, for despreading an output of said adaptive equalizer to obtain an estimate for data symbols of said received users' signals.
- 15 2. The receiver of claim 1, wherein said adaptive equalizer further comprises circuitry for utilizing a blind adaptive algorithm to estimate said transmitted chip sequence of said channel.
3. The receiver of claim 1, wherein said adaptive equalizer further comprises circuitry for utilizing Griffiths' algorithm to converge to a linear minimum mean-
- 20 square error solution.
4. A method for receiving signals in a CDMA telecommunications system, said method comprising the steps of:
  - receiving signals from a CDMA channel;
  - estimating an impulse response of said channel; and
  - 25 adaptively equalizing a signal of said channel by using said estimated impulse response of said channel as a reference and operating to estimate a transmitted chip sequence of said channel to restore orthogonality among said received signals; and

despreading said adaptive equalized signal to obtain an estimate for data symbols of said received signals.

5. The method of claim 4, wherein said step of adaptively equalizing further comprises utilizing a blind adaptive algorithm to estimate said transmitted chip sequence of said channel.
6. The method of claim 4, wherein said step of adaptively equalizing said signal further comprises employing Griffiths' algorithm to converge to a linear minimum mean-square error solution.

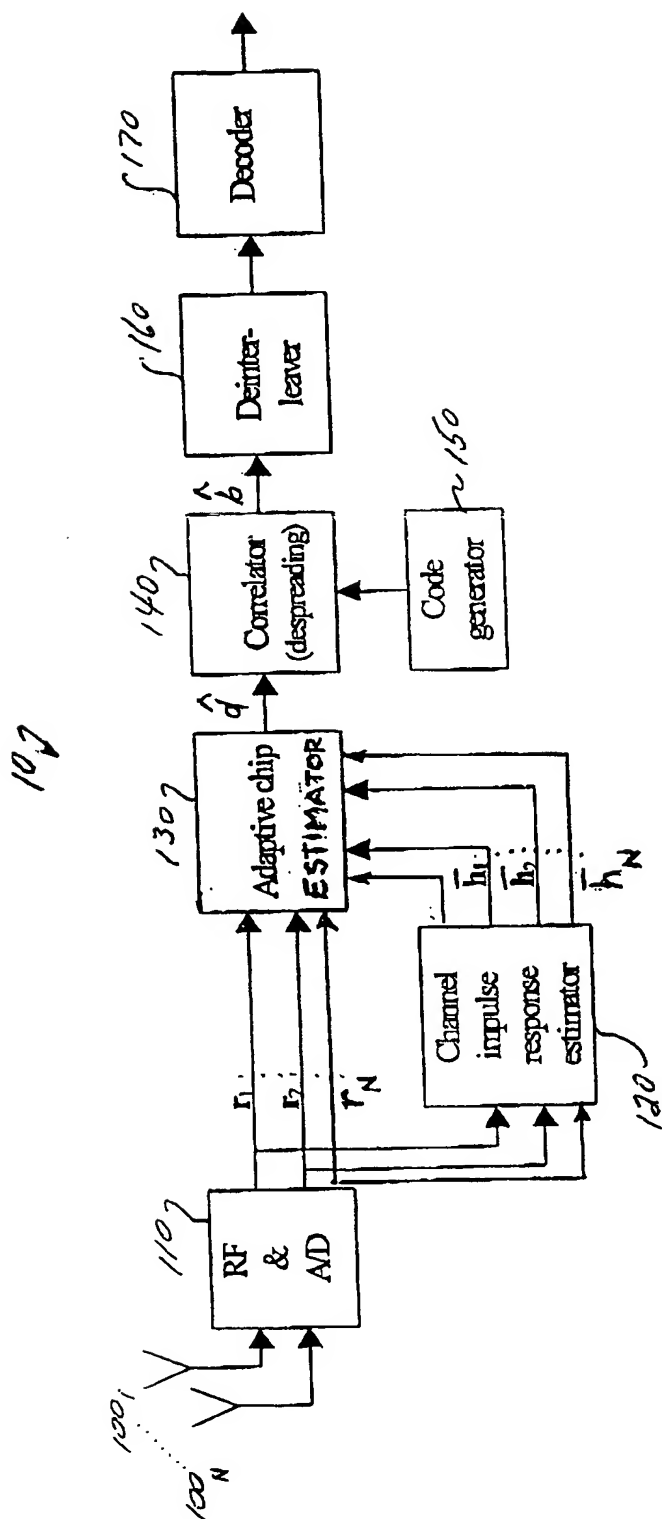


Figure 1



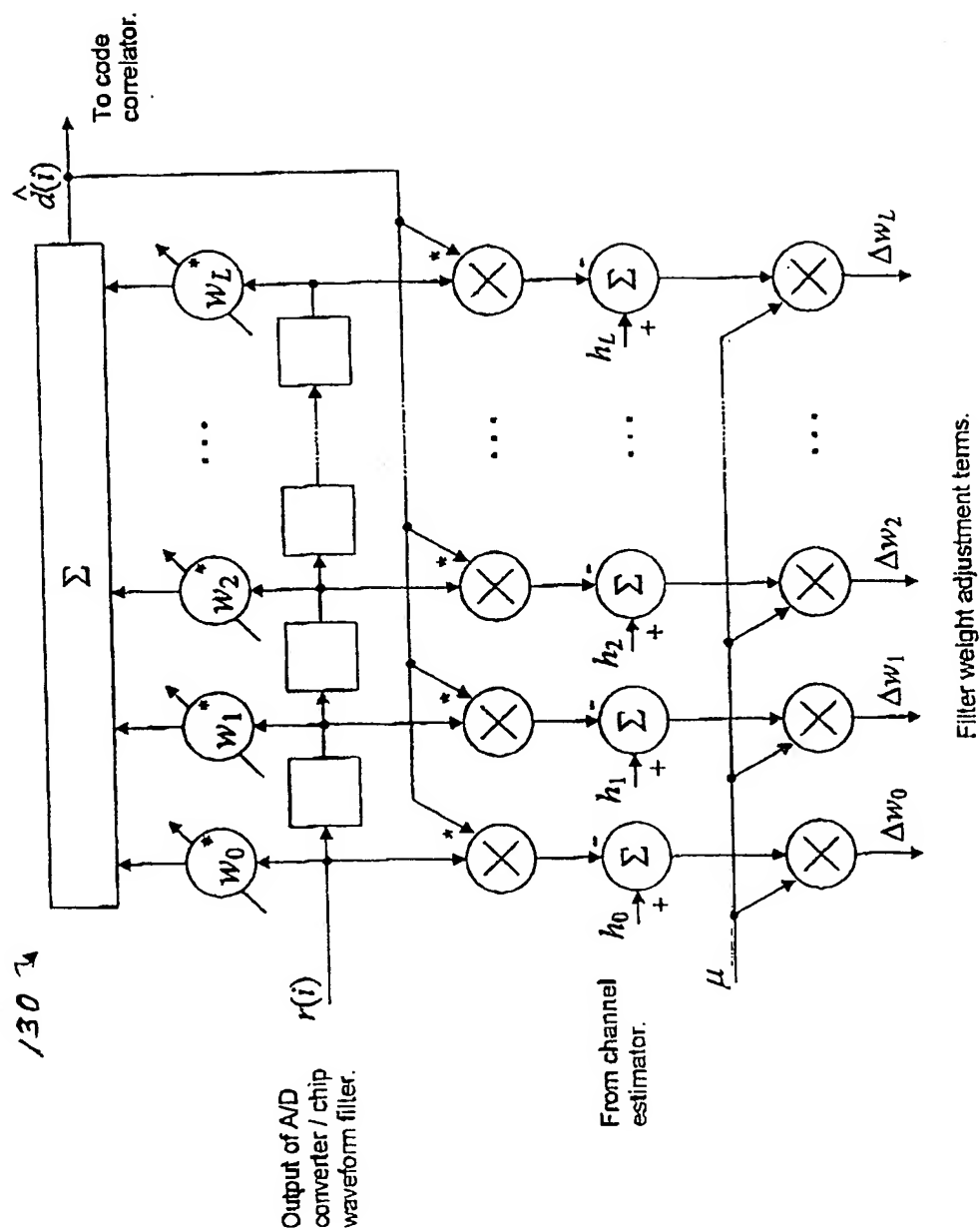


Figure 2

3/5

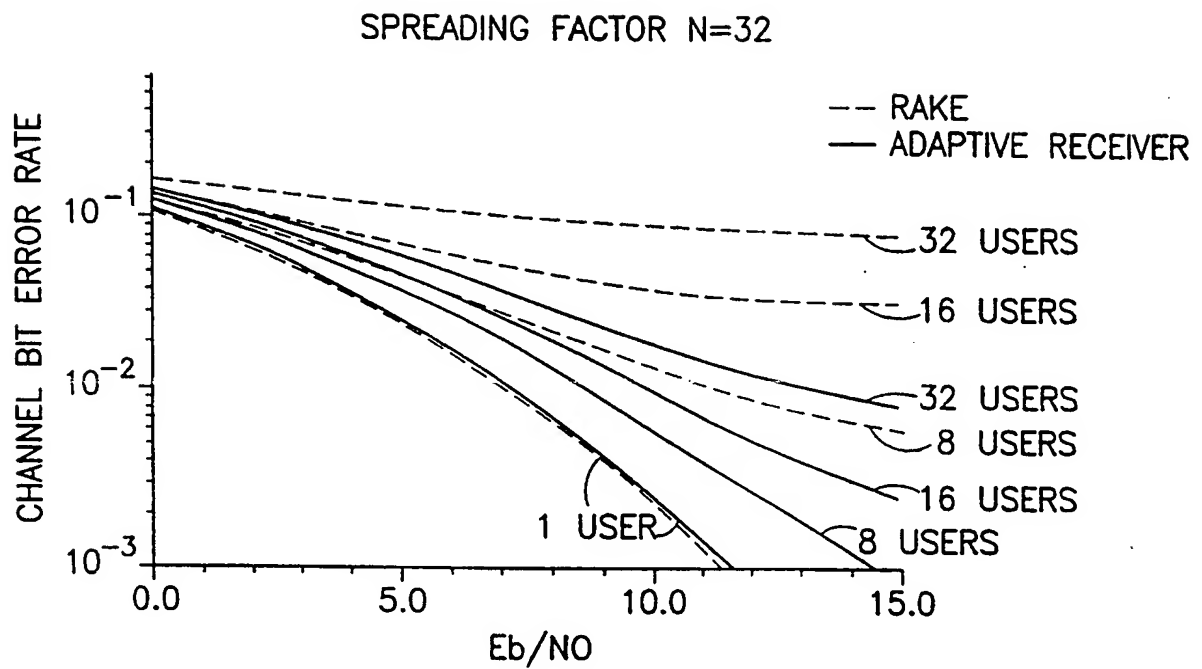


FIG.3

4/5

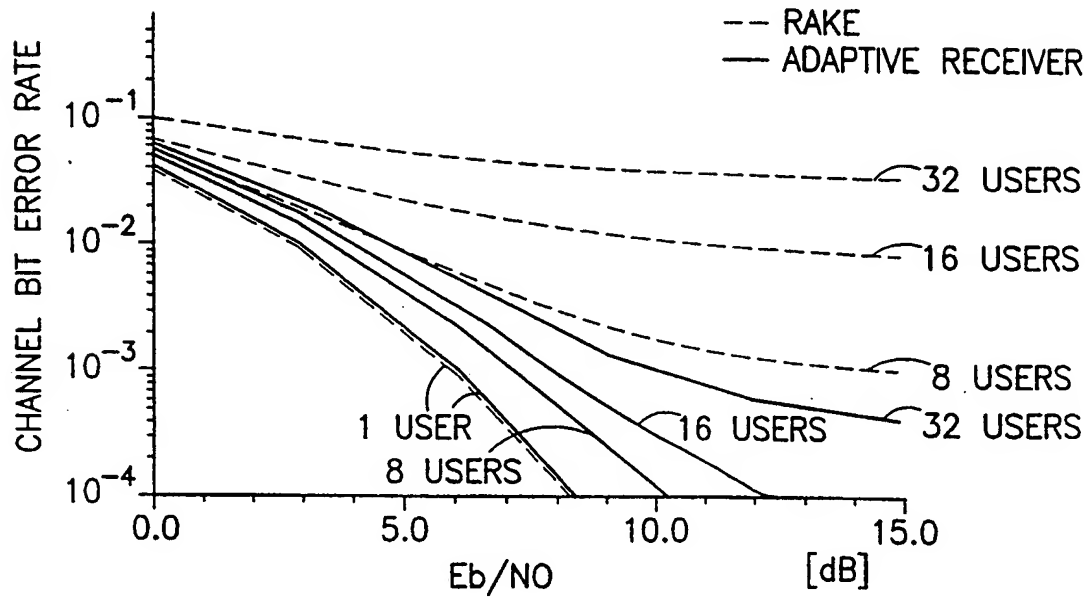
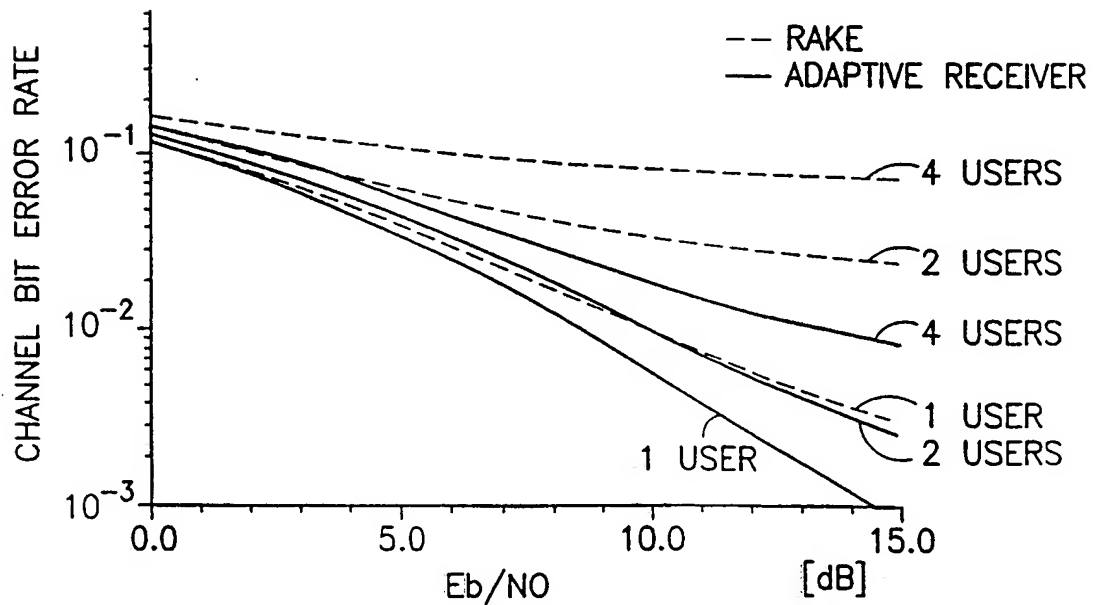
SPREADING FACTOR  $N=32$  (2 DIVERSITY ANTENNAS)

FIG.4

SPREADING FACTOR  $N=4$ 

5/5

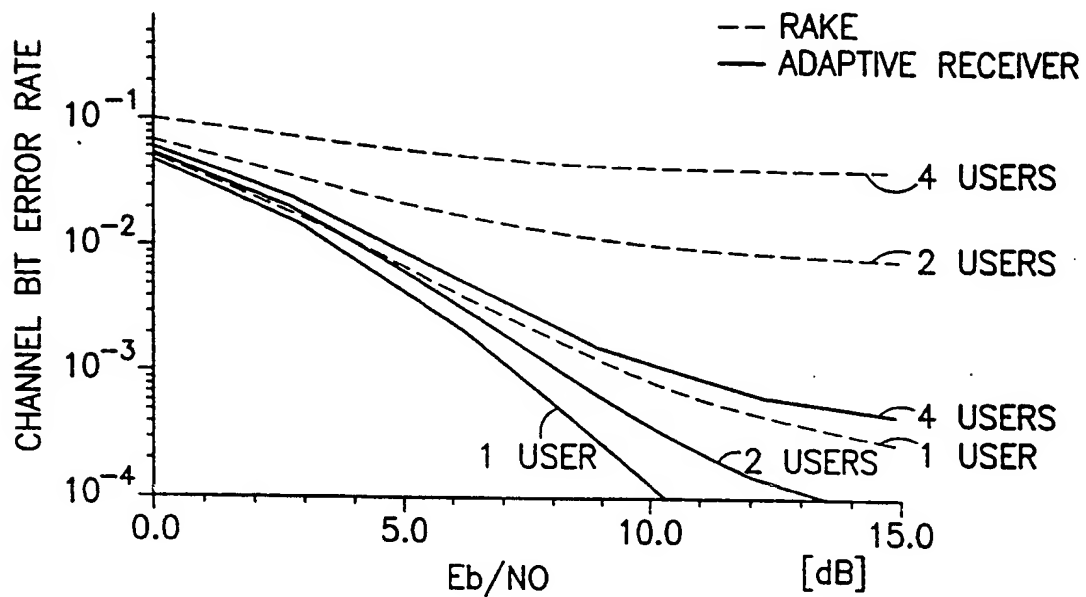
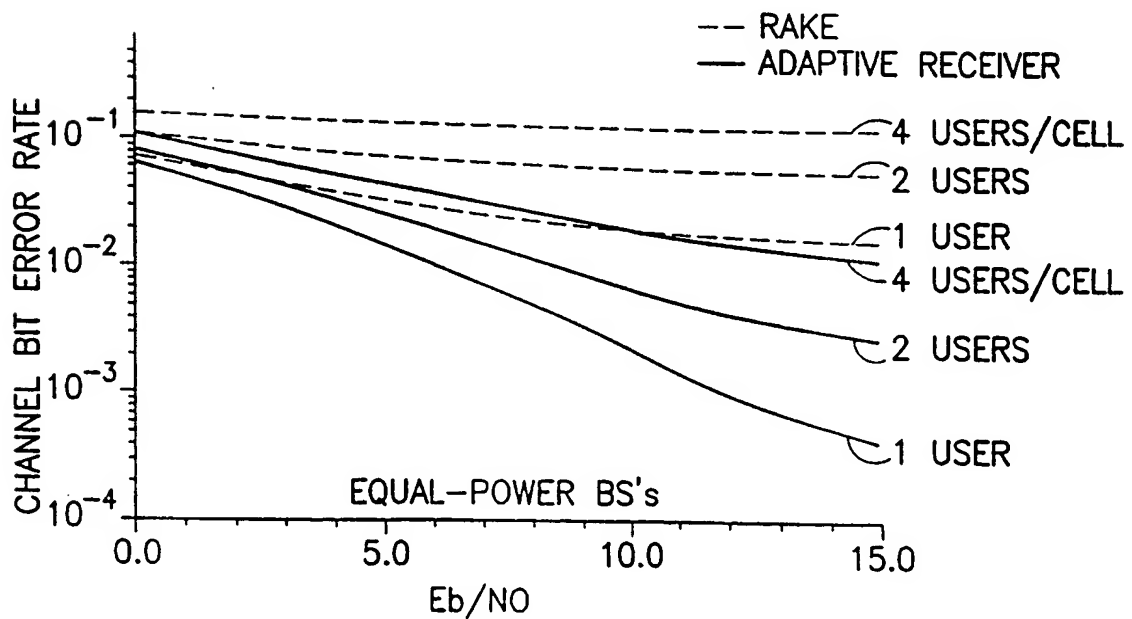
SPREADING FACTOR  $N=4$  (2 DIVERSITY ANTENNAS)

FIG.6

SPREADING FACTOR  $N=4$   
(INTERFERING BS, 2 DIVERSITY ANTENNAS)

# INTERNATIONAL SEARCH REPORT

International application No.

PCT/FI 00/00193

## A. CLASSIFICATION OF SUBJECT MATTER

IPC7: H04B 3/04, H04B 1/06

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC7: H04B, H04L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

SE,DK,FI,NO classes as above

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

| Category* | Citation of document, with indication, where appropriate, of the relevant passages                  | Relevant to claim No. |
|-----------|-----------------------------------------------------------------------------------------------------|-----------------------|
| A         | EP 0604208 A2 (NEC CORPORATION), 29 June 1994<br>(29.06.94), claim 1, abstract<br>--                | 1,4                   |
| A         | US 5615209 A (GREGORY E. BOTTOMLEY), 25 March 1997<br>(25.03.97), column 2, line 42 - line 53<br>-- | 1,4                   |
| A         | EP 0667686 A2 (NEC CORPORATION), 16 August 1995<br>(16.08.95), abstract<br>--<br>-----              | 1,4                   |

☐ Further documents are listed in the continuation of Box C.

☒ See patent family annex.

\* Special categories of cited documents:

"A" document defining the general state of the art which is not considered to be of particular relevance

"E" earlier document but published on or after the international filing date

"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)

"O" document referring to an oral disclosure, use, exhibition or other means

"P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance: the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance: the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art

"&" document member of the same patent family

Date of the actual completion of the international search

Date of mailing of the international search report

1 August 2000

02-08-2000

# INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No.

PCT/FI 00/00193

| Patent document<br>cited in search report | Publication<br>date | Patent family<br>member(s)                                                                                  | Publication<br>date                                                              |
|-------------------------------------------|---------------------|-------------------------------------------------------------------------------------------------------------|----------------------------------------------------------------------------------|
| EP 0604208 A2                             | 29/06/94            | SE 0604208 T3<br>CA 2112020 A,C<br>JP 2605566 B<br>JP 6197032 A<br>US 5519727 A                             | 26/06/94<br>30/04/97<br>15/07/94<br>21/05/96                                     |
| US 5615209 A                              | 25/03/97            | AU 6494896 A<br>BR 9610029 A<br>CA 2227880 A<br>CN 1196842 A<br>EP 0840960 A<br>IL 122989 D<br>WO 9705708 A | 26/02/97<br>06/07/99<br>13/02/97<br>21/10/98<br>13/05/98<br>00/00/00<br>13/02/97 |
| EP 0667686 A2                             | 16/08/95            | AU 681460 B<br>AU 8047694 A<br>CA 2138212 A<br>JP 2734953 B<br>JP 7170243 A<br>US 5598428 A                 | 28/08/97<br>22/06/95<br>17/06/95<br>02/04/98<br>04/07/95<br>28/01/97             |